Seminários de Física

CFUM, LIP-Minho, DF

Effective Methods for Advanced PCB Routing

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15h00

Anfiteatro Dep. Física

Campus de Gualtar

Braga

Summary:

_Most of us are familiar with Moore's Law that predicts that the number of transistors in an Integrated Circuits (IC) would double every 2 years. This made possible to create ICs that can replace thousands (and more) transistors, and thereby increasing the speed and performance but also increasing the heat dissipation.

_Efficiency and power dissipation concerns drove the operation voltage to go lower. As voltage drops, the size and pitch can be reduced, allowing the pin count and density to be increased. All these factors can create big challenges, but also interesting solutions. In this presentation, advanced routing techniques will be presented to address high-speed concerns, routing requirements, and improving reliability and quality on complex designs.

Organizado por:

Universidade do Minho
Escola de Ciências

Centro de Física

Laboratório de Instrumentação e
Física Experimental de Partículas